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PTO/SB/21 (09-04)

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3

Application Number	10/773,809-Conf. #8452
Filing Date	February 5, 2004
First Named Inventor	Kazuya Fujita, et al.
Art Unit	2814
Examiner Name	W. S. Louie

Attorney Docket Number 60,803 (49381)

ENCLOSURES (Check all that apply)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	EDWARDS & ANGELL, LLP		
Signature			
Printed name	George W. Hartnell, III		
Date	April 25, 2005	Reg. No.	42,639

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Docket No.: 60,803 (49381)
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Kazuya Fujita, et al.

Application No.: 10/773,809 - Conf. No.: 8452 Art Unit: 2814

Filed: February 5, 2004 Examiner: W. S. Louie

For: SOLID STATE IMAGING DEVICE, SEMICONDUCTOR WAFER, OPTICAL
DEVICE MODULE, METHOD OF SOLID STATE IMAGING DEVICE
FABRICATION, AND METHOD OF OPTICAL DEVICE MODULE
FABRICATION

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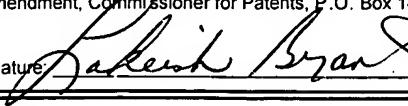
The following is in response to the Office Action mailed March 30, 2005 in connection with the above-referenced application.

The Applicant believes that no extension of time is required since this response is being filed before the expiration of the specified time period. The Applicant, however, conditionally petitions for an extension of time to provide for the possibility that such a petition has been inadvertently overlooked and is required. As provided below, charge Deposit Account No. **04-1105** for any required fee.

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